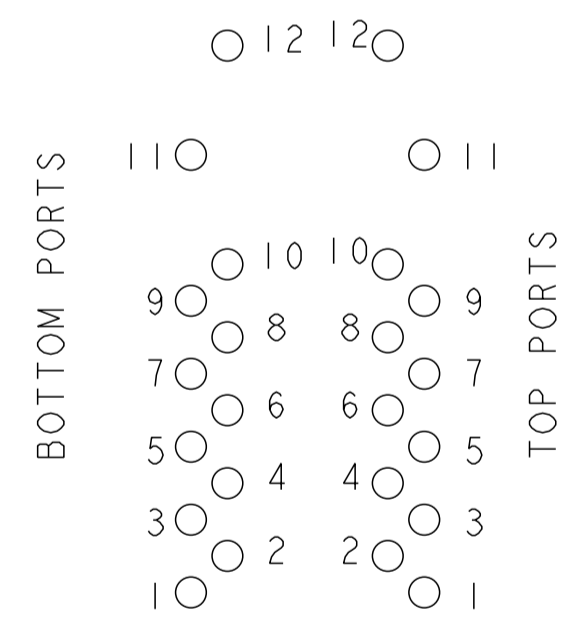
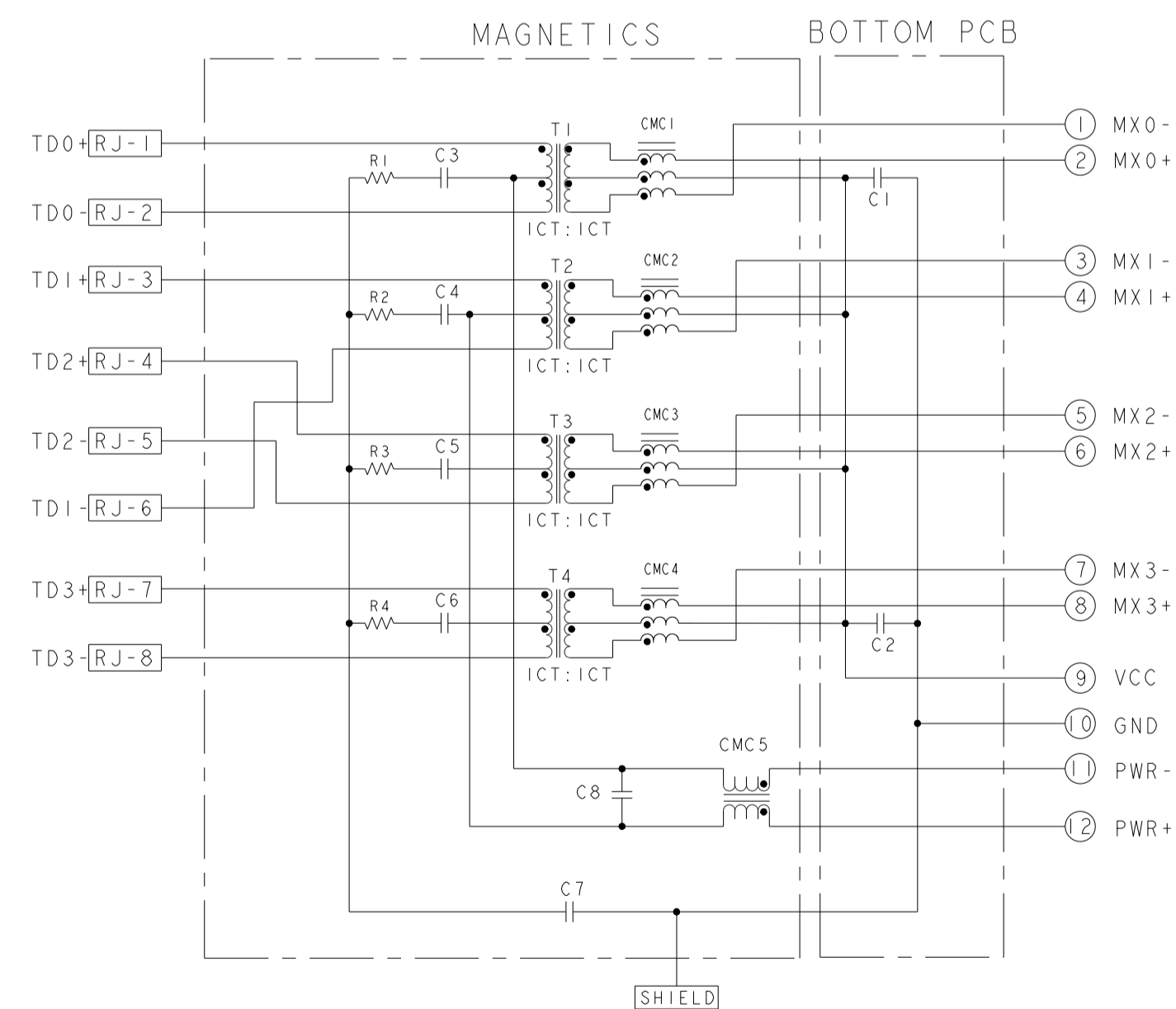


- MATERIALS:**
 PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
 POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
 UNDERPLATE WITH SELECT 1.27um MIN GOLD OR WITH SELECT 0.05um
 MIN GOLD OVER 0.76um MIN PALLADIUM-NICKEL AT MATING INTERFACE
 AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
 ALL PC BOARDS: HIGH TEMPERATURE PCB, TG>170°C
- MAGNETICS**
 APPLICATION: 10/100/1000 BASE-T, PoE PLUS
 IMPEDANCE: 100 OHMS
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
 OPEN CIRCUIT INDUCTANCE (OCL):
 350uH MIN @100kHz, 0.1VRMS, 24mADC BIAS FROM 0°C TO 70°C, RJ1-2 AND RJ3-6.
 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0°C TO 70°C, RJ4-5 AND RJ7-8.
 ALL FOUR PAIRS BI-DIRECTIONAL
 POE CURRENT: 840mADC MAX
 PERFORMANCE @ 25°C:
 INSERTION LOSS (IL): 1.8dB MAX FROM 0.5MHz TO 100MHz
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
 500V/SEC AND WITH ALL PORTS CONNECTED.
- PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN. DATE CODE "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1
- TRP CONNECTOR LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.
- OPERATING TEMP: FROM 0°C TO +70°C.
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.
- DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
- BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.
- THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

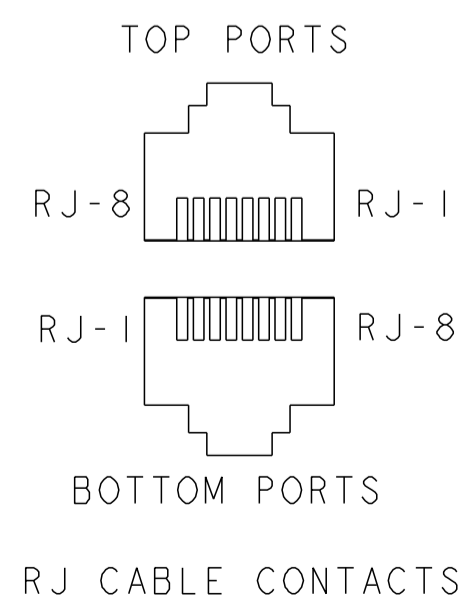
S9HG02C GIGABIT PoE PLUS CIRCUIT

TOP AND BOTTOM PORTS

PIN DESIGNATIONS
 (REPEAT FOR EACH VERTICAL PAIR OF PORTS)



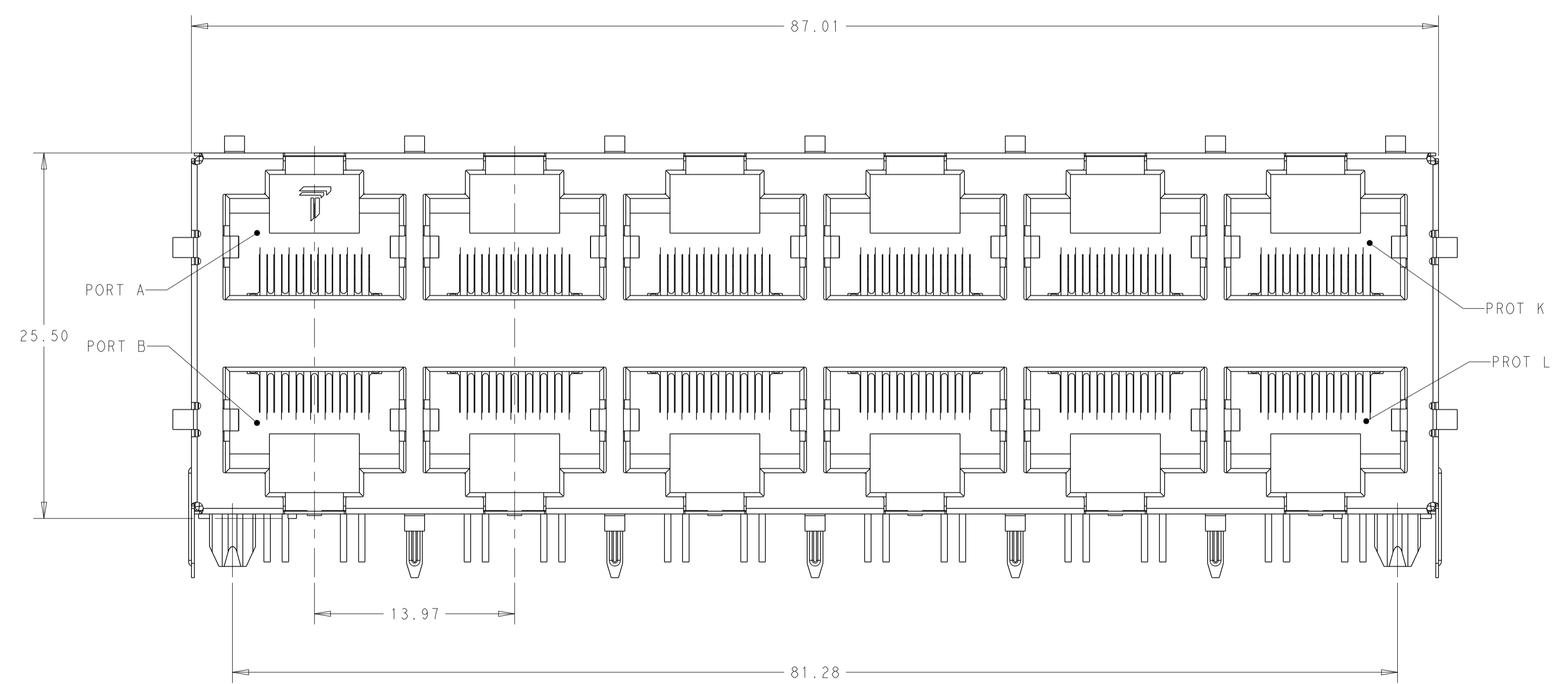
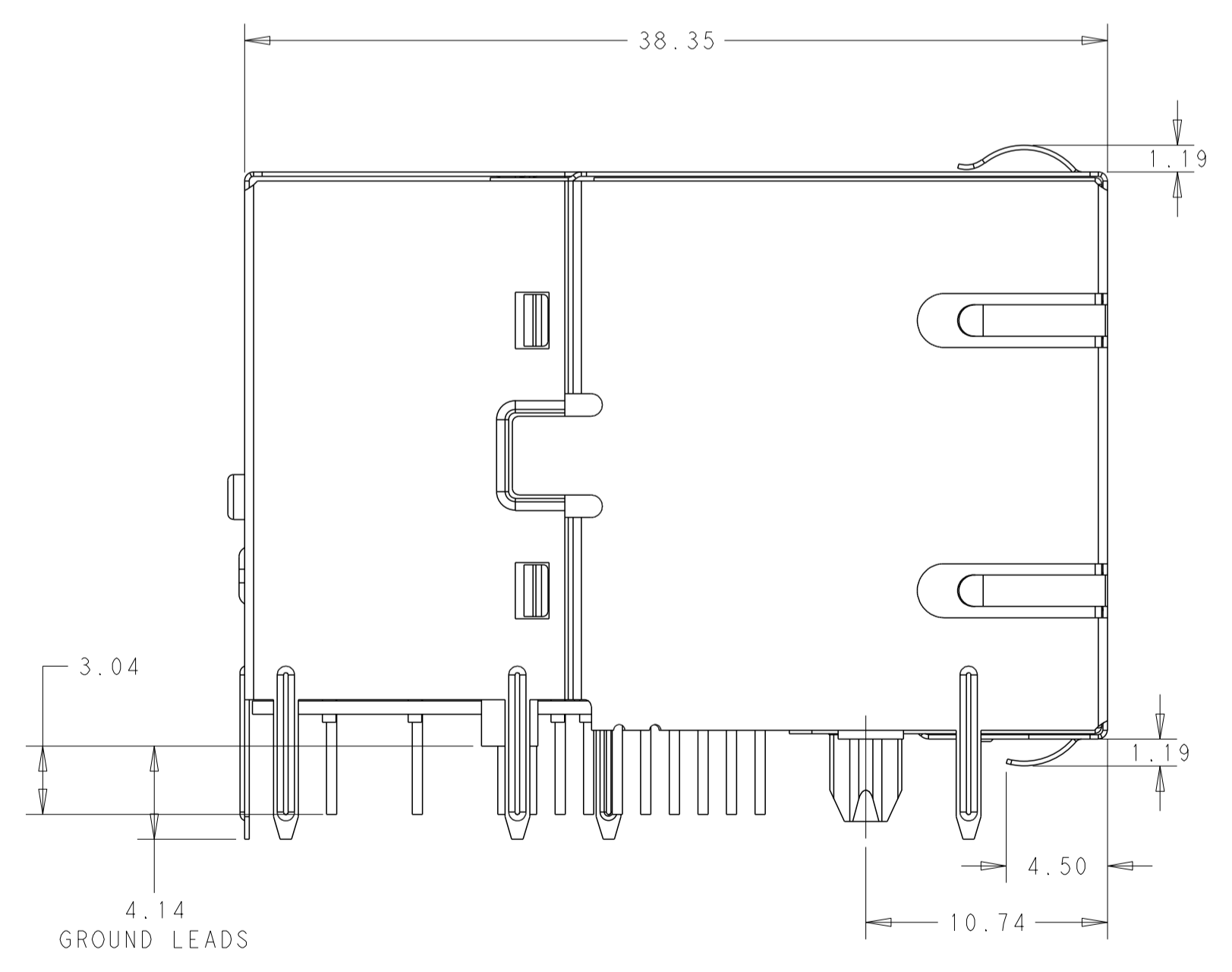
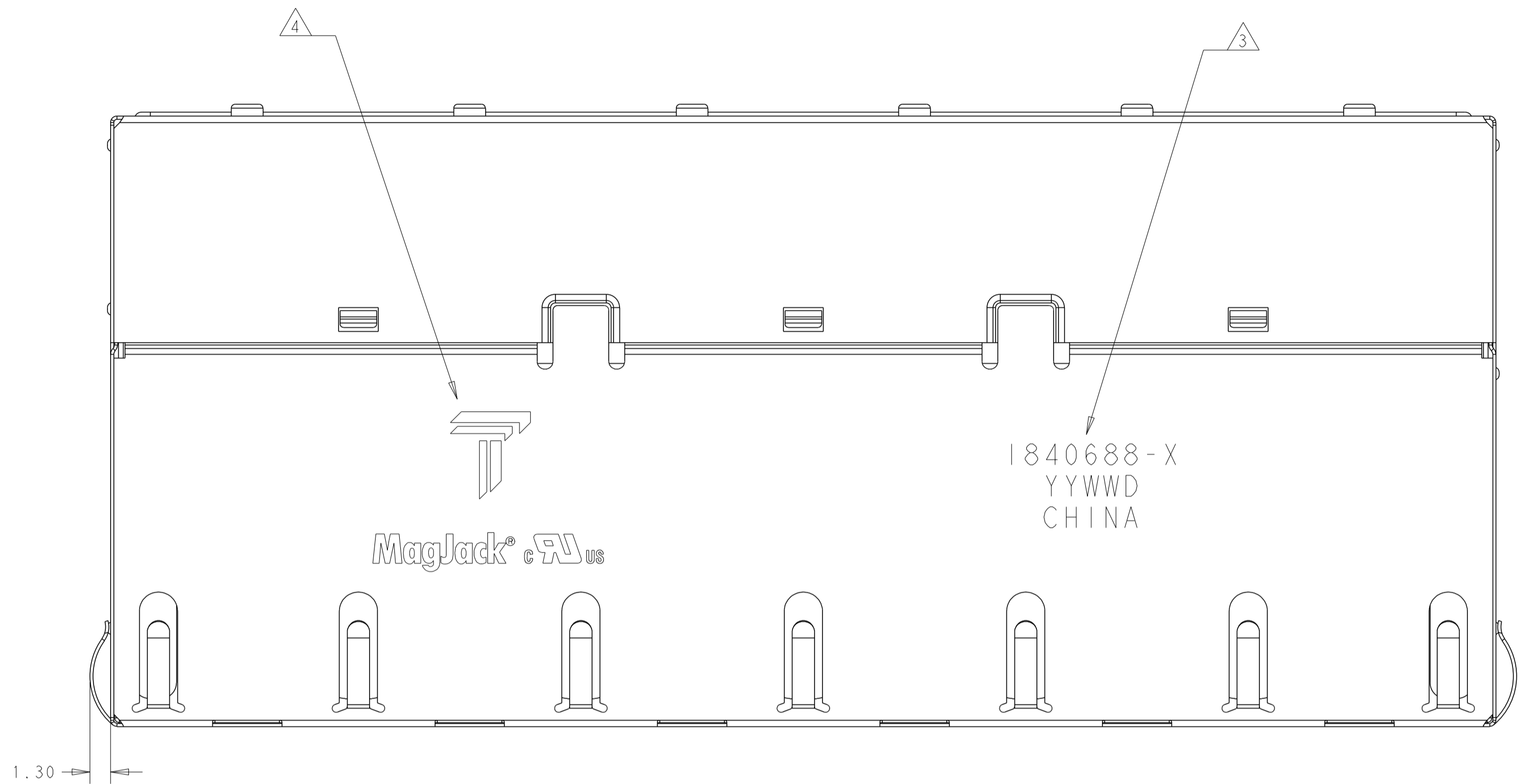
PCB PINS (TOP VIEW, COMPONENT SIDE)




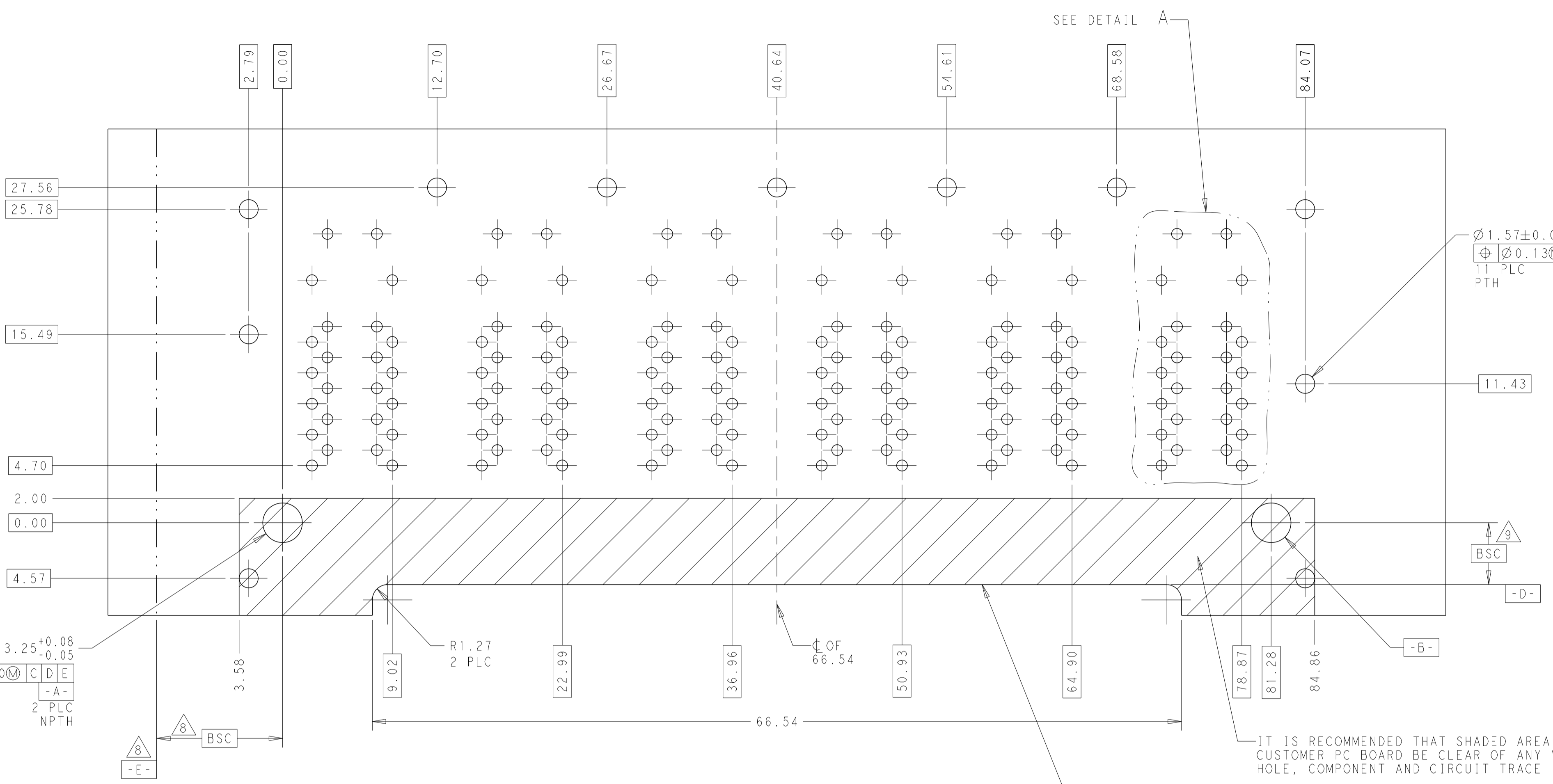
C1-C2=10nF, CAPACITORS
 C3-C6=22nF, 100V CAPACITORS
 C7=1000pF, 2kV, CAPACITOR
 C8=68nF, 100V CAPACITOR
 R1-R4=75 OHMS, RESISTORS

1840688-1
 PART NUMBER

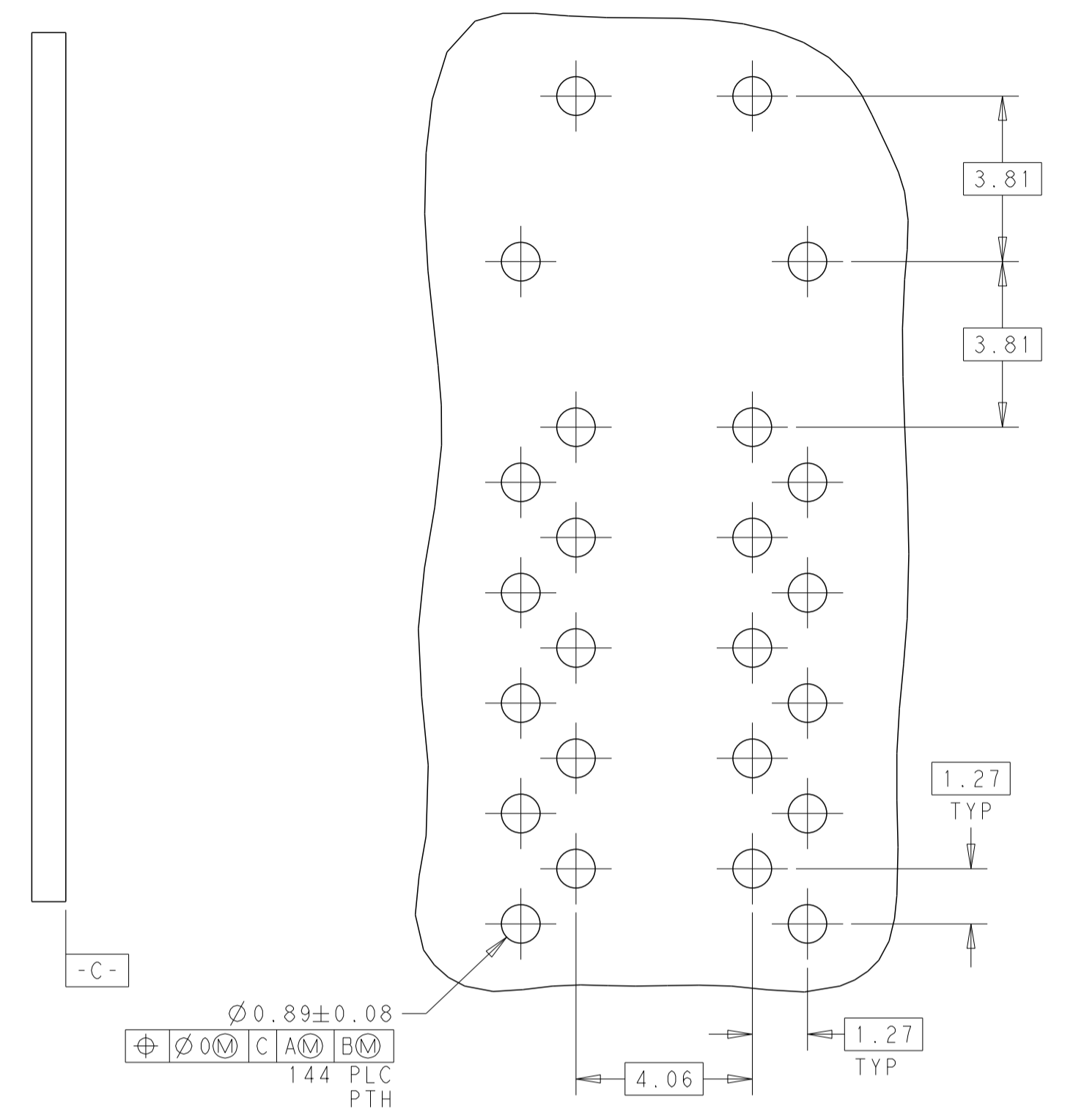
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: GABRIEL Z. TERRY LIN 06JAN2012	DONGGUAN CHINA
		CHK: TONY YUAN 06JAN2012	
		APVD: KEITH ZHU 06JAN2012	DESC: 2X6 S9HG02C GIGABIT PoE+ W/O LED MODEL NAME: MAGJACK STACK PoE+
DIMENSIONS: mm 	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ±	SIZE: A1 CAGE CODE: C=1840688 DRAWING NO:	RESTRICTED TO: - SCALE: 4:1 SHEET: 1 OF 3 REV: 9
PRODUCT SPEC: 108-104004	APPLICATION SPEC:	CUSTOMER DRAWING	



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: 06 JAN 2012 GABRIEL Z. TERRY LIN		 trp CONNECTOR a bel group DONGGUAN CHINA
DIMENSIONS: mm		CHK: 06 JAN 2012 TONY YUAN		
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD: 06 JAN 2012 KEITH ZHU		DESC: 2X6 S9HG02C GIGABIT PoE+ W/O LED
0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ±0.25 4 PLC ± ANGLES ± APPLICATION SPEC ±		MODEL NAME: MAGJACK STACK PoE+		SIZE: A1
PRODUCT SPEC: 108-104004		CUSTOMER DRAWING		SCALE: 4:1 SHEET 2 OF 3 REV 9



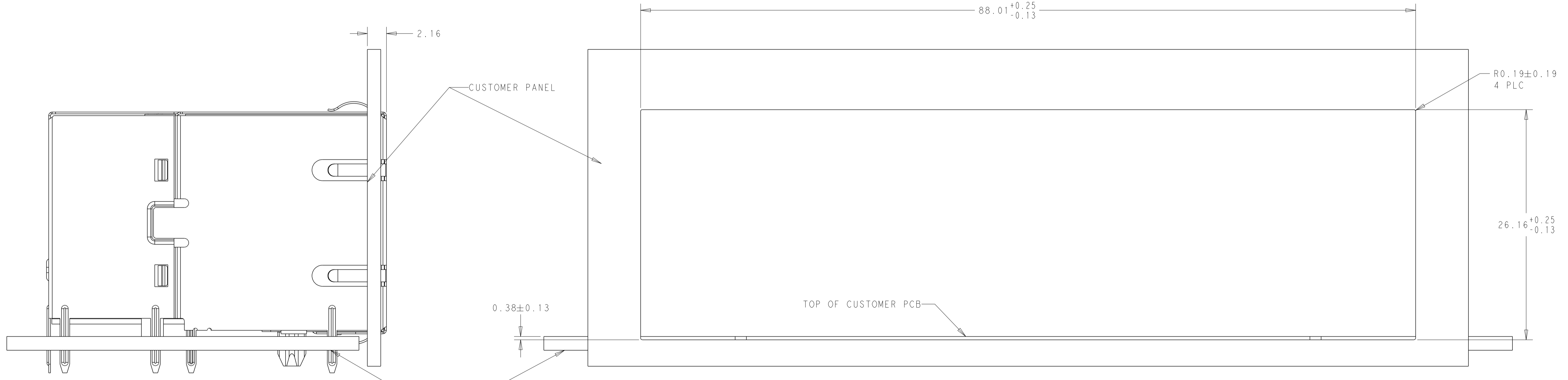
RECOMMENDED PCB LAYOUT
COMPONENT SIDE VIEW



DETAIL A
SCALE 8:1


IT IS RECOMMENDED THAT SHADED AREA OF CUSTOMER PC BOARD BE CLEAR OF ANY VIA HOLE, COMPONENT AND CIRCUIT TRACE

RECOMMENDED PCB FRONT EDGE CUTOUT:
THE BOTTOM SHIELD GROUND TABS(FRONT) SHOULD NOT BE SEATED ON THE TOP OF THE PCB



SHOWN WITH PANEL AND
CUSTOMER PCB ASSEMBLY

SUGGESTED PANEL CUTOUT DIMENSIONS

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWR: GABRIEL.Z/LIGHT.CHEW 06JAN2012 CHK: TONY YUAN 06JAN2012 APVD: KEITH ZHU 06JAN2012		 DONGGUAN CHINA
DIMENSIONS: mm TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± PRODUCT SPEC APPLICATION SPEC		MODEL NAME model_name1 model_name2		
		CUSTOEMR DRAWING		RESTRICTED TO SCALE NTS SHEET 3 OF 3 REV 8